

PATENT NUMBER

U.S. **UTILITY** Patent Application

O.I.P.E.

PATENT DATE

4
SCANNED

Q.A

APPLICATION NO. 09/598384	CONT/PRIOR F	CLASS 365	SUBCLASS	ART UNIT 2818	EXAMINER
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Yasuaki Hirano

Erase method for nonvolatile semiconductor storage device and row decoder circuit for fulfilling the method

PTO-2040
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ISSUING CLASSIFICATION

[illegible]

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
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